



# **RS2G86 Dual 2-Input Exclusive-OR Gate**

## **1 FEATURES**

- Operating Voltage Range:1.65V to 5.5V
- Low Power Consumption:1µA (Max)
- Operating Temperature Range: -40°C to +125°C
- Inputs Accept Voltage to 5.5V
- High Output Drive: ±24mA at Vcc=3.0V
- Micro SIZE PACKAGE: MSOP8

### **2 APPLICATIONS**

- AV Receiver
- Blu-ray Player and Home Theater
- Digital Picture Frame (DPF)
- High-Speed Data Acquisition and Generation
- Personal Navigation Device (GPS)
- Portable Media Player

LOGIC SYMBOL



#### IEE/IEC LOGIC SYMBOL



### **3 DESCRIPTIONS**

The RS2G86 Dual 2-input exclusive-OR gate is designed for 1.65V to 5.5V  $V_{\text{CC}}$  operation.

The RS2G86 device performs the Boolean function  $Y=\overline{A}B+A\overline{B}$  in positive logic. The device is fully specified for partial-power-down applications using loff. The loff circuitry disables the outputs, preventing damaging current backflow through the device when it is powered down.

The RS2G86 is available in Green MSOP8 packages. It operates over an ambient temperature range of -40°C to +125°C.

#### **Device Information**<sup>(1)</sup>

PART NUMBER	PACKAGE	BODY SIZE (NOM)		
RS2G86	MSOP8	3.00mm×3.00mm		

(1) For all available packages, see the orderable addendum at the end of the data sheet.

## **4 FUNCTION TABLE**

INP	OUTPUT	
Α	В	Y
Н	Н	L
L	Н	Н
Н	L	Н
L	L	L

H=High Voltage Level L=Low Voltage Level



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## **5 Revision History**

Note: Page numbers for previous revisions may different from page numbers in the current version.

Version	Change Date	Change Item	
A.1	2021/01/21	Initial version completed	
A.2	2021/12/21	Added the TAPE AND REEL INFORMATION	
A.3	2023/09/20	1. Update V <sub>IH</sub> , V <sub>IL</sub> PARAMETER 2. Update ESD Ratings 3. Add MSL on Page 5@RevA.2	
A.3.1	2024/02/29	Modify packaging naming	



## 6 PACKAGE/ORDERING INFORMATION (1)

PRODUCT	ORDERING NUMBER	TEMPERATURE RANGE	PACKAGE LEAD	PACKAGE MARKING <sup>(2)</sup>	MSL <sup>(3)</sup>	PACKAGE OPTION
RS2G86	RS2G86XM	-40°C ~+125°C	MSOP8	RS2G86	MSL3	Tape and Reel,4000

NOTE:

(1) This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the right-hand navigation.

(2) There may be additional marking, which relates to the lot trace code information(data code and vendor code), the logo or the environmental category on the device.

(3) MSL, The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications.



## 7 PIN CONFIGURATIONS

TOP VIEW



#### **PIN DESCRIPTION**

PIN		I/O TYPE <sup>(1)</sup>	FUNCTION
MSOP8			FUNCTION
1	1A	I	Channel 1 logic input
2	1B	I	Channel 1 logic input
3	2Y	0	Logic level output
4	GND	-	Ground
5	2A	I	Channel 2 logic input
6	2B	I	Channel 2 logic input
7	1Y	0	Logic level output
8	Vcc	-	Power Supply

(1) I=input, O=output.



#### 8 Specifications 8.1 Absolute Maximum Ratings <sup>(1)</sup>

over operating free-air temperature range (unless otherwise noted)<sup>(1)(2)</sup>

			MIN	MAX	UNIT
Vcc	Supply voltage range		-0.5	6.5	V
VI	Input voltage range <sup>(2)</sup>		-0.5	6.5	V
Vo	Voltage range applied to any output in the high-impedan	ce or power-off state <sup>(2)</sup>	-0.5	6.5	V
Vo	Voltage range applied to any output in the high or low st	-0.5	V <sub>CC</sub> +0.5	V	
I <sub>IK</sub>	Input clamp current VI<0			-50	mA
Іок	Output clamp current	V <sub>0</sub> <0		-50	mA
lo	Continuous output current			±50	mA
	Continuous current through $V_{CC}$ or GND			±100	mA
ALθ	Package thermal impedance <sup>(4)</sup>		170	°C/W	
τJ	Junction temperature <sup>(5)</sup>			150	°C
T <sub>stg</sub>	Storage temperature		-65	150	°C

(1) Stresses beyond those listed under *Absolute Maximum Ratings* may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions* is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

(2) The input and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.

(3) The value of V<sub>cc</sub> is provided in the *Recommended Operating Conditions table*.

(4) The package thermal impedance is calculated in accordance with JESD-51.

(5) The maximum power dissipation is a function of  $T_{J(MAX)}$ ,  $R_{\theta JA}$ , and  $T_A$ . The maximum allowable power dissipation at any ambient temperature is  $P_D = (T_{J(MAX)} - T_A) / R_{\theta JA}$ . All numbers apply for packages soldered directly onto a PCB.

#### 8.2 ESD Ratings

The following ESD information is provided for handling of ESD-sensitive devices in an ESD protected area only.

			VALUE	UNIT
		Human-body model (HBM), MIL-STD-883K METHOD 3015.9	±2000	V
V(ESD)	Electrostatic discharge	Charged-device model (CDM), ANSI/ESDA/JEDEC JS-002-2018	±1000	V
		Machine Model (MM), JESD22-A115C (2010)	±200	V



#### **ESD SENSITIVITY CAUTION**

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.



## 9 ELECTRICAL CHARACTERISTICS

over recommended operating free-air temperature range (TYP values are at  $T_A = +25$  °C, Full=-40 °C to 125 °C, unless otherwise noted.) <sup>(1)</sup>

## 9.1 Recommended Operating Conditions

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	MAX	UNIT	
Coursel course literan	M	Operating	1.65	5.5	- v	
Supply voltage	Vcc	Data retention only	1.5		v	
		V <sub>CC</sub> =1.65V to 1.95V	0.65 x V <sub>CC</sub>			
		V <sub>CC</sub> =2.3V to 2.7V	1.7			
High-level input voltage	VIH	V <sub>CC</sub> =3V to 3.6V	2		- V	
		V <sub>CC</sub> =4.5V to 5.5V	0.7 x V <sub>CC</sub>		-	
	VIL	V <sub>CC</sub> =1.65V to 1.95V		0.35 x V <sub>CC</sub>		
		V <sub>CC</sub> =2.3V to 2.7V		0.6		
Low-level input voltage		V <sub>CC</sub> =3V to 3.6V		0.8	- V	
		V <sub>CC</sub> =4.5V to 5.5V		0.3 x V <sub>CC</sub>		
Input voltage	VI		0	5.5	V	
Output voltage	Vo		0	Vcc	V	
		V <sub>CC</sub> =1.8V± 0.15V,2.5V ± 0.2V		20		
Input transition rise or fall	t <sub>r</sub> , t <sub>f</sub>	V <sub>CC</sub> =3.3V± 0.3V		10	ns/V	
		V <sub>CC</sub> =5V± 0.5V		5		
Operating temperature	TA		-40	+125	°C	

(1) All unused inputs of the device must be held at  $V_{CC}$  or GND to ensure proper device operation.



#### 9.2 DC Characteristics

PA	RAMETER	TEST CONDITIONS	Vcc	TEMP	MIN <sup>(2)</sup>	<b>TYP</b> <sup>(3)</sup>	MAX <sup>(2)</sup>	UNIT
		Іон = -100μА	1.65V to 5.5V		Vcc-0.1			
		I <sub>OH</sub> = -4mA	1.65V		1.2			
	Maria	I <sub>OH</sub> = -8mA	2.3V	Full	1.9			v
	Vон	I <sub>OH</sub> = -16mA	2)/	Full	2.4			v
		I <sub>OH</sub> = -24mA	- 3V		2.3			
		I <sub>OH</sub> = -32mA	4.5V		3.8			
		Ιοι = 100μΑ	1.65V to 5.5V				0.1	
		I <sub>OL</sub> = 4mA	1.65V				0.45	v
	N/	I <sub>OL</sub> = 8mA	2.3V	<b>_</b>			0.3	
	V <sub>OL</sub>	I <sub>OL</sub> = 16mA	3V Full				0.4	
		I <sub>OL</sub> = 24mA					0.55	
		I <sub>OL</sub> = 32mA	4.5V				0.55	
	A an Dimmuta	V <sub>1</sub> =5.5V or GND 0V to 5.5V		+25°C		±0.1	±1	۸
lı	A or B inputs		00 10 5.50	Full			±5	μA
			0)(	+25°C		±0.1	±1	
	l <sub>off</sub>	$V_1$ or $V_0$ =5.5V	0V	Full			±10	μA
				+25°C		0.1	1	
	lcc	V <sub>I</sub> =5.5V or GND, I <sub>0</sub> =0	1.65V to 5.5V	Full			10	μA
One input at V <sub>cc</sub> -0.6V,        Δlcc      Other inputs at V <sub>cc</sub> or        GND		3V to 5.5V	Full			500	μA	

(1) All unused inputs of the device must be held at  $V_{\text{CC}}$  or GND to ensure proper device operation.

(2) Limits are 100% production tested at 25°C. Limits over the operating temperature range are ensured through correlations using statistical quality control (SQC) method.

(3) Typical values represent the most likely parametric norm as determined at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration.

#### 9.3 AC Characteristics

(T<sub>A</sub> =-40°C to +125°C, typical values are at T<sub>A</sub> = +25°C, unless otherwise noted.) <sup>(1)</sup>

PARAMETER	SYMBOL	TEST CONDITIONS		MIN <sup>(2)</sup>	<b>TYP</b> <sup>(3)</sup>	<b>MAX</b> <sup>(2)</sup>	UNIT
		Vcc=1.8V±0.15V	$C_L=30pF, R_L=1k\Omega$		14.2		
Propagation Dolay	+ .	Vcc=2.5V±0.2V	$C_L=30pF, R_L=500\Omega$		11.1		20
Propagation Delay	t <sub>pd</sub>	Vcc=3.3V±0.3V	$C_L=50pF, R_L=500\Omega$		6.7		ns
		Vcc=5V±0.5 V	$C_L=50pF, R_L=500\Omega$		5.3		
Input Capacitance	Ci	Vcc=3.3V	VI=VCC or GND		4		pF
		Vcc=1.8V			20		
Power dissipation	C	Vcc=2.5V	f=10MHz		21		<b>"</b> Г
capacitance	C <sub>pd</sub>	Vcc=3.3V			22		pF
		V <sub>CC</sub> =5V			25		

(1) All unused inputs of the device must be held at  $V_{\text{CC}}$  or GND to ensure proper device operation.

(2) This parameter is ensured by design and/or characterization and is not tested in production.

(3) Typical values represent the most likely parametric norm as determined at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration.



## **10 Parameter Measurement Information**



TEST	S1
tplh/tphl	Open
tplz/tpzl	VLOAD
tрнz/tрzн	GND

Vcc	INPUTS		V <sub>M</sub>	Maria	C.	RL	V۵
	Vı	t <sub>r</sub> /t <sub>f</sub>	₩М	VLOAD	CL	κL	V۵
1.8V±0.15V	Vcc	≤2ns	Vcc/2	2 x Vcc	30pF	1kΩ	0.15V
2.5V±0.2V	Vcc	≤2ns	Vcc/2	2 x Vcc	30pF	500Ω	0.15V
3.3V±0.3V	3V	≤2.5ns	1.5V	6V	50pF	500Ω	0.3V
5V±0.5V	Vcc	≤2.5ns	$V_{CC}/2$	2 x V <sub>CC</sub>	50pF	500Ω	0.3V



VOLTAGE WAVEFORMS PROPAGATION DELAY TIMES INVERTION AND NONINVERTING OUTPUTS

VOLTAGE WAVEFORMS ENABLE AND DISABLE TIMES LOW-AND HIGH-LEVEL ENABLING

NOTES: A.  $C_L$  includes probe and jig capacitance.

- B. Waveform 1 is for an output with internal conditions such that the output is low, except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high, except when disabled by the output control.
  C. All input pulses are supplied by generators having the following characteristics: PRR ≤ 10 MHz, Z<sub>0</sub> = 50 Ω.
- D. The outputs are measured one at a time, with one transition per measurement.
- E.  $t_{PLZ}$  and  $t_{PHZ}$  are the same as  $t_{dis}$ .
- F.  $t_{PZL}$  and  $t_{PZH}$  are the same as  $t_{en}$ .
- G.  $t_{PLH}$  and  $t_{PHL}$  are the same as  $t_{pd}$ .
- H. All parameters and waveforms are not applicable to all devices.

#### Figure 1. Load Circuit and Voltage Waveforms



#### 11 PACKAGE OUTLINE DIMENSIONS MSOP8<sup>(3)</sup>





RECOMMENDED LAND PATTERN (Unit: mm)





Symbol	Dimensions I	n Millimeters	Dimensions In Inches			
	Min	Max	Min	Max		
A <sup>(1)</sup>	0.820	1.100	0.032	0.043		
A1	0.020	0.150	0.001	0.006		
A2	0.750	0.950	0.030	0.037		
b	0.250	0.380	0.010	0.015		
с	0.090	0.230	0.004	0.009		
D <sup>(1)</sup>	2.900	3.100	0.114	0.122		
e	0.650(	BSC) <sup>(2)</sup>	0.026(BSC) <sup>(2)</sup>			
E <sup>(1)</sup>	2.900	3.100	0.114	0.122		
E1	4.750	5.050	0.187	0.199		
L	0.400	0.800	0.016	0.031		
θ	0°	6°	0°	6°		

NOTE:

1. Plastic or metal protrusions of 0.15mm maximum per side are not included.

2. BSC (Basic Spacing between Centers), "Basic" spacing is nominal.

3. This drawing is subject to change without notice.



## **12 TAPE AND REEL INFORMATION**

### **REEL DIMENSIONS**

**TAPE DIMENSION** 



NOTE: The picture is only for reference. Please make the object as the standard.

#### KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
MSOP8	13"	12.4	5.20	3.30	1.50	4.0	8.0	2.0	12.0	Q1

NOTE:

1. All dimensions are nominal.

2. Plastic or metal protrusions of 0.15mm maximum per side are not included.



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